



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT***Complete if Known*

Application Number	10/749,262
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First Named Inventor	Jin-Tae Oh
Art Unit	2434
Examiner Name	Powers, William S
Attorney Docket Number	3364P161

**Sheet**

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**NON PATENT LITERATURE DOCUMENTS**

Examiner (Initials) <sup>1</sup>	Cite Min <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or	T <sup>2</sup>
		Cho, Y.H., et al., "Specialized Hardware for Deep Network Packet Filtering," 12th Conference on Field Programmable Logic and Applications, Montpellier, France, Sep. 2002, pp. 452-461.	
		Sidhu, R.P.S., et al., "String Matching on Multicontext FPGAs using Self-Reconfiguration," Proceedings, 1999 ACM/SIGDA 7th Intl. Symposium on Field Programmable Gate Arrays, Feb. 1999, pp. 217-226.	

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<sup>1</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>1</sup>Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.